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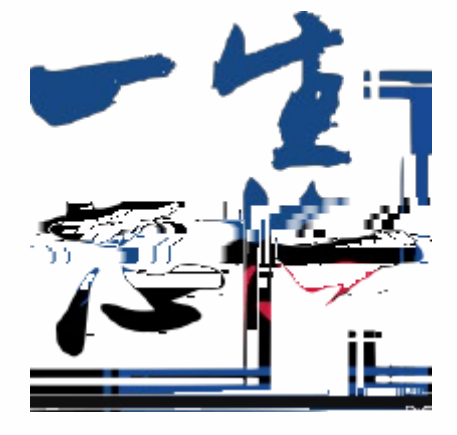
- Chisel Scala

RISC-V



- Chisel
- Chiseltest

past



使用Chisel

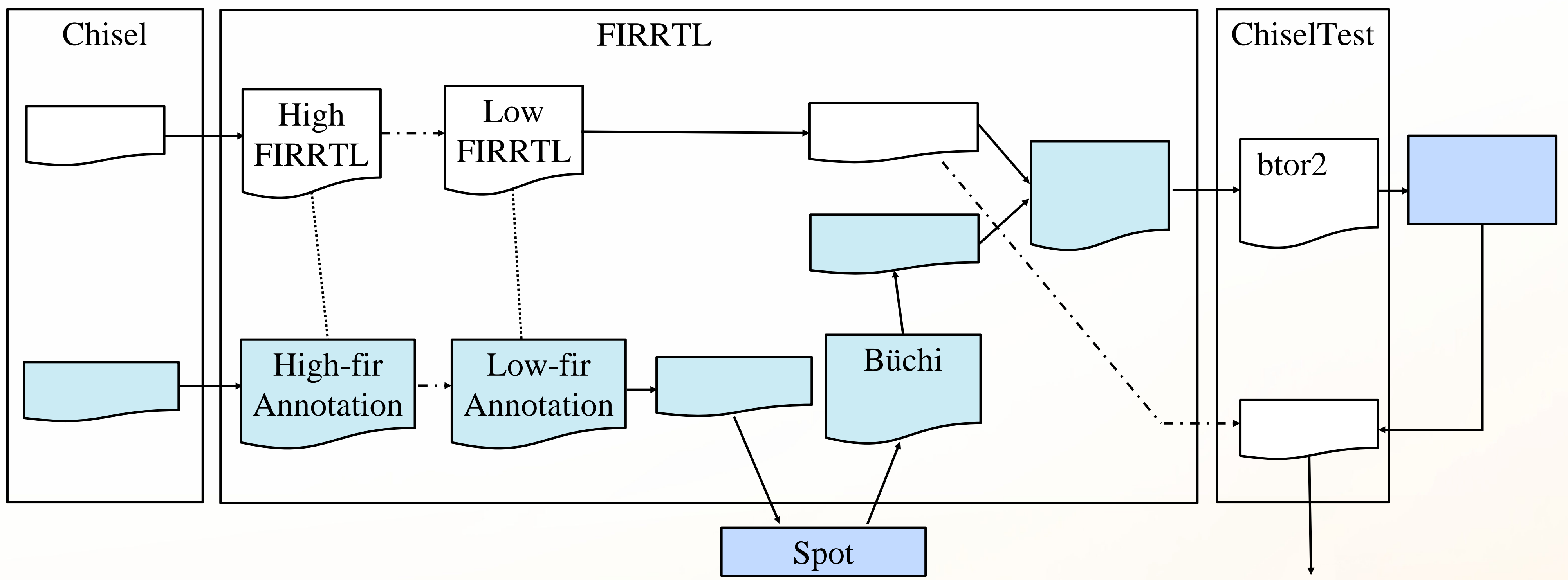
- Chisel SVA的序列/

序列算子与SVA

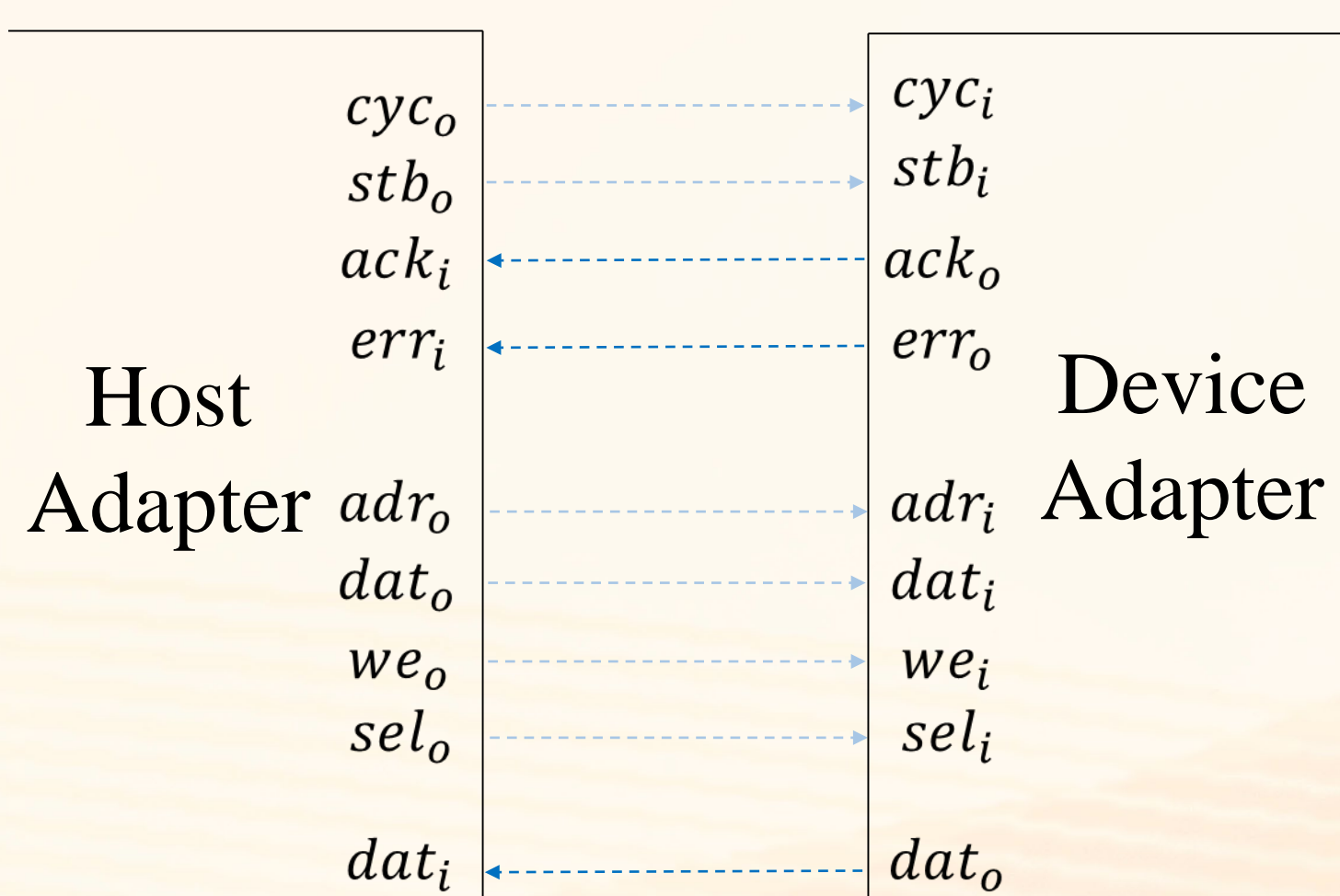
				序列析取	0次	-
SVA	u	##0	##1	or	[*0]	[*1:\$]
Ours	u	##0	##1		[*0]	[*1:\$]

算子与SVA

					nexttime	always	s_eventually	s_until
SVA	->	not	and	or	nexttime	always	s_eventually	s_until
Ours	->	!	&&		X	G	F	U



1. 利用Target-Annotation
2. spot
- 3.
4. L2S



Wishbone

在Chisel

```
assert( stb |-> (stb U ack) || G stb )
```

- Chisel
- 支持了assume/assert
- safety property
- liveness property )
- bug, 也支持使用 K-induction 等